A4 EProm

Source Pin	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44
Assignment	Write Enable	A18	A17	A7	A6	A5	A4	A3	A2	A1	A0	CE1	GND	OE	Q0	Q8	Q1	Q9	Q2	Q10	Q3	Q11	VCC	Q4	Q12	Q5	Q13	Q6	Q14	Q7	Q15	GND	BYTE	A16	A15	A14	A13	A12	A11	A10	A9	A8	A19	WP
Destination Pin	NC	4D Pin 18	4D Pin 16	4D Pin 14	4D Pin 12	5D Pin 18	5D Pin 16	5D Pin 14	5D Pin 12	6D Pin 18	6D Pin 16	GND	GND	8A Pin 14, 9D Pin 13	J3 A8	J3 A9	J3 A10	J3 A11	J3 A12	J3 A13	J3 A14	J3 A15	5V	J3 C5	J3 B13	J3 B12	J3 B11	J3 B10	J3 B9	J3 B8	J3 B7	GND	J3 CS-10, J3 D4-9	6D Pin 5	6D Pin 3	5D Pin 9	5D Pin 7	5D Pin 5	5D Pin 3	4D Pin 9	4D Pin 7	4D Pin 5	4D Pin 3	NC
Assignment	NC	1Y1	1Y2	1Y3	1Y4	1Y1	1Y2	1Y3	1Y4	1Y1	1Y2																							2Y3	2Y4	2Y1	2Y2	2Y3	2Y4	2Y1	2Y2	2Y3	2Y4	

A8 EPROM

Source Pin	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44
Assignment	Write Enable	A18	A17	A7	A6	A5	A4	A3	A2	A1	A0	CE1	GND	OE	Q0	Q8	Q1	Q9	Q2	Q10	Q3	Q11	VCC	Q4	Q12	Q5	Q13	Q6	Q14	Q7	Q15	GND	BYTE	A16	A15	A14	A13	A12	A11	A10	A9	A8	A19	WP
Destination Pin	NC	6D Pin 14	6D Pin 12	7D Pin 18	7D Pin 16	7D Pin 14	7D Pin 12	8D Pin 18	8D Pin 16	8D Pin 14	8D Pin 12	GND	GND	6A Pin 14, 9D Pin 13	J3 A16	J3 A17	J3 A18	J3 A19	J3 A20	J3 D20	J3 D19	J3 D18	5V	J3 C19	J3 C20	J3 B20	J3 B19	J3 B18	J3 B17	J3 B16	J3 B15	GND	J3 CS-10, J3 D4-9	8D Pin 9	8D Pin 7	8D Pin 5	8D Pin 3	7D Pin 9	7D Pin 7	7D Pin 5	7D Pin 3	6D Pin 9	6D Pin 7	NC
Assignment	NC	1Y3	1Y4	1Y1	1Y2	1Y3	1Y4	1Y1	1Y2	1Y3	1Y4																							2Y1	2Y2	2Y3	2Y4	2Y1	2Y2	2Y3	2Y4	2Y1	2Y2	

4D TTL Input Lines

Source Pin	2	4	6	8	11	13	15	17
Assignment	1A1	1A2	1A3	1A4	2A1	2A2	2A3	2A4
Destination	J3 C4	J3 C3	J3 C2	J3 C1	J3 A1	J3 D1	J3 D2	J3 D3

5D TTL Input Lines

Source Pin	2	4	6	8	11	13	15	17
Assignment	1A1	1A2	1A3	1A4	2A1	2A2	2A3	2A4
Destination	J3 B1	J3 B2	J3 B3	J3 B4	J3 A5	J3 A4	J3 A3	J3 A2

6D TTL Input Lines

Source Pin	2	4	6	8	11	13	15	17
Assignment	1A1	1A2	1A3	1A4	2A1	2A2	2A3	2A4
Destination	J3 B5	J3 B6	J3 C4	J3 C3	J3 D2	J3 D3	J3 A7	J3 A6

7D TTL Input Lines

Source Pin	2	4	6	8	11	13	15	17
Assignment	1A1	1A2	1A3	1A4	2A1	2A2	2A3	2A4
Destination	J3 C2	J3 C1	J3 B1	J3 B2	J3 A3	J3 A2	J3 A1	J3 D1

8D TTL Input Lines

Source Pin	2	4	6	8	11	13	15	17
Assignment	1A1	1A2	1A3	1A4	2A1	2A2	2A3	2A4
Destination	J3 B3	J3 B4	J3 B5	J3 B6	J3 A7	J3 A6	J3 A5	J3 A4

9D TTL Lines

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Assignment	A	B	C	G2A	G2B	G1	Y7	GND	Y6	Y5	Y4	Y3	Y2	Y1	Y0	Vcc
Destination	J3 C18	J3 D17	J3 D16	GND	GND	GND		GND		9A CE, 6A CE	9B CE, 6B CE	9C CE, 6C CE	8A CE, 4A CE	8B CE, 4C CE	8C CE, 4C CE	